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CHARACTERIZATION OF CIRCUITS IN GRID **OBTAINED BY REGULAR AND SEMI-REGULAR TESSELLATIONS**

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ABSTRACT: In this paper circuits in grids which are obtained by using plane tessellations are observed. Isomorphism and congruence of circuits in these grids is defined in natural way. Conection between these relations is discussed.

1. INTRODUCTION

A tesselation of plane is a covering of the plane by using polygons. It is known that there are exactly eleven ways to cover plane by using regular polygons. Three of these are regular tessellations, where each vertex is surrounded by identical regular polygons (see fig.1). The other eight are semi-regular tessellations, in which each vertex is surrounded by an identical cycle of regular polygons (see fig.2).



Figure 1. The three regular tesselations



Figure 2. The eight semi-regular tesselations

In this way eleven infinite periodic grids are obtained. (This grids are plane representation of infinite plane graphs.). Let G be one of obtained grids. A circuit of the lenght m in a grid G is a oriented clo-sed path without repeated vertices, containg m edges.

A circuit C in the grid G determines a simple polygon which consists of the edges of C. We will say that a circuit C_1 is congruent to a circuit C_2 iff polygons determined by circuits C_1 and C_2 are congruent polygons. Also, in natural way we define an isomorphism of circuits in the grid G which is obtained by regular and semi-regular tessellations. Let C_1 and C_2 be the circuits in the grid G. Then: the circuits C_1 and C_2 are isomorphic_circuits iff there exists a congruence transformation T such that:

1) T maps the grid G into itself and 2) T maps a polygon determined by the circuit C_1 into polygon determined by circuit C2.

Also we say that simple polygons A and B in the grid G are isomorphic polygons iff circuits determined by A and B are isomorphic circuits.

2. WORD REPRESENTATION OF CIRCUITS

Let G be one of grids obtained by using tessellations. Grid G is periodic, Let us determine period of grid G. If n is the number of edges in period of G then the num-ber of oriented edges is 2n and we shall denote these oriented edges (vectors) by $v(0),v(1),\ldots,v(2n-1)$. In this way for any oriented edge of the grid G there is corres-ponding-uniquely determined vector from the set $v=\{v(0), v(1), \dots, v(2n-1)\}$

ponding-uniquely determined vector from the set v={v(0), v(1),...,v(2n-1)}. Let A and B be points in the grid G, and P oriented path of lenght t, from A to B. If path P consists of ori-ented edges v(i1),v(i2),...,v(it) respectively, then the word f(P)=i1i2...it which corresponds to path P is uni-quely determined. Specialy, for i=1 f(v(i))=i. Let A^K be the set of all words of lenght k over the alphabet A= ={0,1,...,2n-1} and A^{*}=UA^K k>0. Then denote by A" the set of all words which corres-ponds to oriented pats in the grid G. That means: a6A^k=>

exists path P such that f(P)=a. If the word a=iji2...it is from A", then a determines the path v(ij)...v(it) such that f(P)=a. The circuit C of lenght n determines 2n closed oriented paths, depending on the choice of the initial vertex and the orientation on the choice of the initial vertex and the orientation of the circuit. A function f maps these 2n oriented paths into 2n words of the set A". Let us denote the set of these 2n words by Q(C) (for circuit C). Let T be isometry which maps grid G into itself. Let $T(v(i))=v(i^{-}) =0,1,2,$..., 2n-1 then $(0^{-},1^{-},...,(2n-1)^{-})$ is permutation of (0,1,..., 2n-1). Transformation T maps path $P=v(i_1)v(i_2)...$ $v(i_t)$ into path T(P) such that $T(P)=T(v(i_1)v(i_2)...v(i_t))$ $=T(v(i_1))T(v(i_2))...T(v(i_t))=v(i_1)v(i_2)...v(i_t)$ or $f(T(P))=i_1j_2...i_t$. Let A" be the set of all words which correspond to circuits in the grid G. Also every word a from A" (a=1)

circuits in the grid G. Also every word a from A^m (a=i₁...i_t) determines circuit C=v(i₁)v(i₂)...v(i_t) (which

determines simple polygon with edges of C). Let a and b be words from A^m. We say that a and b are in the relation α iff circuits, which are determined by words a and b, are isomorphic circuits.

LEMMA 1: Relation α is equivalence relation.

PROOF: The set τ of all congruence transformations which map grid G into itself is a group. Specialy: If T=I (identical mapping) then for a,bEQ(C) \Rightarrow

aαb.

In the set of vectors $\{v(0), v(1), \ldots, v(2n-1)\}$ we define relation p by: $v(i) v(j) \Leftrightarrow$ exists isometry T which maps the grid G into itself such that

T(v(i)) = T(v(j))

LEMMA 2: Relation p is a relation of equivalence. PROOF: Directly from definition.

Also, we say that: ipj iff v(i)vv(j). \rightarrow If P is a path from point A to point B then vector AB is equal to the vector sum of oriented edges which the path P contains.

LEMMA 3: Word $a=i_1i_2...i_t$ from A" is from A" (or $v(i_1)v(i_2)...v(i_t)$ is circuit) iff 1) $v(i_1)+v(i_2)+...+v(i_t)=0$ (vector summ) and 2) $v(i_j)+v(i_{j+1})+...+$ v(i_{j+k})≠0 for k<j+k≤ t.

3. ALGORITHM FOR COUNTING NONISOMORPHIC CIRCUITS

Using observations from previous section we can propose one common algorithm for determination numbers of nonisomorphic circuits on each of grids obtained by tessellations (regular and semi-regular).

Let e_1 and e_2 be the vectors from $\{v(0), v(1), ...$ v(2n-1)} which are not colinear. Now, we determine co-ordinates of each vector from { $v(0), v(1), \dots, v(2n-1)$ }

with respect to vectors e_1 and e_2 . Let $v(i)=\alpha_1e_1+\beta_1e_2$ (i=0,...,2n-1) then follows (from Lemma 3):

LEMMA 4: If a=iji2,..., it is word from A" then

 $\sum_{i=0}^{2n-1} \alpha_i^{1(i)=0} \text{ and } \sum_{i=0}^{2n-1} \beta_i^{1(i)=0}$ i=0

where l(i) is the number of occurences of character i in the word $a=i_1i_2...i_t$. CONDITION 3 will be called CONDI-TION-G.

LEMMA 5: If P is an oriented path in the grid G then: P is a circuit iff:

the word f(P) satisfies CONDITION-G and
 no subword b of a satisfies CONDITION-G.

We use following input data and their notations:

 Number of vectors in period of G.
 Number of continuations of each vector denoted by C. (Vector v(i) is a continuation of a vector v(j) if word ji is from A").
3) Continuations of each vector. Corresponding cha-

racters for continuations of vector v(i) denoted by

tial vector can be any vector. It is clear that if the word $a=i_1i_2...i_t$. Is from A" and $i_1 \rho j_1$ then there exists a word $b=j_1j_2...j_t$ such that a c b. That means: the number of initial vectors can be equals to the number of equivalence classes with respect to relation ρ . In this way can be reduce the computation time.

5) Initial vectors-corresponding characters denoted by $Iv(1), Iv(2), \dots, Iv(I)$. 6) Number of transformations (of τ). 7) One isometry of first class which maps grid into

itself.

8) One isometry of second class which maps grid into itself. 9) For every vector v(i) its opposite vector v(j).

(i(i) = -v(j)).

Note: for grid 3*.6 isometry of first class no exists so 7 is identical mapping.

Let $a=i_1i_2...i_t$ be word from A", and let a(j) denoted the word $i_ji_{j+1}...i_t$ 1 $\leq j \leq t$. If CONDITION-G is not satis-fied for any $j(1\leq j \leq t)$ then we call the word $i_1i_2...i_t$ addable. It is clear that $i_1i_2...i_t$ can be completed to a word $i_1i_2...i_m$ (m>t), representing a circuit iff i_1i_2 ... i_t is addable. Also it is obvious that $a=i_1i_2...i_t$ denotes a circuit iff a(j) satisfied CONDITION-G only for j=1.

All words that are α -equivalent to a word a representing a circuit we can obtain using 6,7,8,9 (see input data). We consider only the equivalent words begining by one of initial vectors (input data-5) and sort them in lexicographic order. We choose the first word a ´as a representative of this class. Hence, if the word a is equal to a', then word a represents a circuits of lenght t and print it.

Our algorithm can be conveniently explained using two phases: extend and reduce. These phases correspond to the addable and nonaddable cases respectively,

READ (t) FOR k=1 to I DO BEGIN ij=Iv(k); m:=1 RÉPEAT IF iji2...im is addable THEN extend ELSE IF iji2...im is representative of a nonisomorphic circuits THEN print iji2...im reduce UNTIL m=1 END where extend = BEGIN m:=m+1; i_m :=c(i_{m-1} ,1) END reduce = WHILE i_m =c(i_{m-1} C) and m22 DO m:=m-1 IF m≠1 THEN BEGIN t:=0

REPEAT t:=t+1 UNTIL im=c(im-1,t) im:=c(im-1,t+1) END

Data obtained by proposed algorithm will be given in next section, k(t) denoted the number of nontisomorphic circuits lenght of t,

Grids which is obtained by tessellations 6^3 , 4^4 , 3^6 are not specially treated, but they have been observed in the papers: |1|, |2|, |3|. The algorithm presented here could be also directly applied to these a rids.

CONNECTION BETWEEN ISOMORPHISM AND 4. CONGRUENCE OF CIRCUITS

It is clear that if C_1 and C_2 are isomorphic circuits then C_1 and C_2 are congruent circuits. In this section we will-show that for grids obtained by tessellations $3^3.4^2$, $3^2.4.3.4$, 3.4.6.4, 3.6.3.6, 3.12^2 , 4.6.12, 4.8^2 (all se-mi-regular except 3'.6) is satisfied: if circuits C_1 and C_2 are congruent circuits then they are isomorphic circuits. For grids obtained by regular tessellations previous statament follows obviously because of that they are not specialy treated.

In proofs of following lemmas we will use:

LEMMA 6: Let M=M1M2...Mt and N=N1N2...Nt be congruent polygons such that $M_{j1}\equiv N_{j1}, M_{j2}\equiv N_{j2}, M_{j2}\equiv N_{j3}$ (for some integres ij,i2,i3 from (1,2,...,t), then: if points M_{j1}, M_{j2} and M_{j3} are not colinear then $M_{j}\equiv N_{j1}$ for all iG(1,2,...,t).

We shall denote by i(i,j) the angle between vectors v(i) and v(j). By r(M) will denoted the word mi.2..., determined by a simpl polygon M=M1M2...Mt such that mi= \sim $f(M_{i}M_{i+1})$.

LEMMA 7: Let G be the grid obtained by tessellation $3^3.4^2$, then: if M and N are congruent polygons in grid G then $r(M)\alpha r(N)$.

PRODF: Equivalence classes with respect to relation ρ are: I={0,5} II={1,4,9,6} III={2,3,7,8} (see fig.3) Let M=M1M2...Mt and N=N1N2...Nt are congruent polygons in the grid G and r(M)=m1m2...mt and r(N)=n1n2...nt.



1-case:m₁,n₁GI then m₁m₂...m_t α Om₂...m_t and n₁n₂...n_t α On₂ ...n_t (words Om₂...m_t and On₂...n_t exist because m₁ and n₁ are from same equivalence class) if m₂=n₂ then by Lemma 6 m₁=n₁ for i=3,...,t if m₂/n₂ then we apply reflection in

a line determined by vector v(0) which maps grid G into itself. The image of $Om_2' \dots m_t'$ is $Om_2' \dots m_t'$.

0 1 2 3 4 5 6 7 8 9 σ₀ (0 6 8 7 9 5 1 3 2 4)

since $(0, m_2^2)=(0, n_2^2)$ we have $m_1^2=n_1^2, m_1m_2...m_t \alpha 0n_2^2...m_t \alpha 0n_2^2...m_t^2$ that means (by Lemma 6) $n_3^2=m_3^2,...,n_t^2=m_t^2$ or $m_1m_2...m_t \alpha n_1n_2...n_t$.

2-case: mini6||then mim2...mtaim2...mt and nin2...ntain2

...n; }(1,m2)=}(1,n2)≫>m2=n2 If m2=n2=1 then we continue until m1≠1 but then using

Lemma 6 $m_i = n_i$ or $m_1 m_2 \dots m_t \alpha 1 m_2 \dots m_t = 1 n_2 \dots n_t \alpha n_1 n_2 \dots n_t$. 3-case: $m_1, n_1 GIII$ then $m_1 m_2 \dots m_t \alpha 2 m_2 \dots m_t$ and $n_1 n_2 \dots$

· ·

3-case: m₁, m₁--- $n_{t}\alpha^{2}n_{2}^{2}...n_{t}^{2}$ $i(2,m_{2}^{2})=i(2,n_{2}^{2})$ if $m_{2}^{2}=n_{2}^{2}$ then clearly $m_{1}^{2}=n_{1}^{2}$ for i=3,...,t and $m_{1}m_{2}...m_{t}^{2}=n_{1}^{2}$ $m_{t}\alpha n_{1}n_{2}...n_{t}$ if $m_{2}^{2}\neq n_{2}^{2}$ then, let be for example, $m_{2}^{2}=3$ and $n_{2}^{2}=4$, now $i(3,m_{3}^{2})=i(4,n_{3}^{2}) \implies m_{3}^{2}=6$ and $n_{3}^{2}=3$ $i(2,m_{2}^{2})=i(2,n_{3}^{2}) \implies m_{3}^{2}=6$ and $n_{3}^{2}=3$ $i(2,m_{2}^{2})=i(2,n_{3}^{2}) \implies m_{3}^{2}=6$ and $n_{3}^{2}=3$ $m_{1}^{2}=236$ and $2n_{2}^{2}...n_{t}^{2}=243$ but $236 \alpha 243$

4-case: $m_1 \in [n_1 \in]$ then $m_1 m_2 \dots m_t \alpha Om_2 \dots m_t$ and $n_1 n_2 \dots$

 $n_{t} = 0 n_{t} = 0$ $n_{t} = 0 n_{t} = 0$ $j(0, m_{2}) = j(1, n_{2}) \Rightarrow m_{2} = 1$ and $n_{2} = 5$ continuing we have $0m_{2} \dots m_{t} = 0$ $m_{t} = 0.154$ and $1n_{2} \dots n_{t} = 1540$, but $0.154 \alpha 1540$

5-case: m16|, n16|||then m1m2...mtaOm2...mt and n1n2...

D-case: miej, mjej, men mim2...mi 2...mi 2...mi 2...mi 2...mi 2...mi 3...mi 3...mi

6-case: m16|| , n16||| then m1m2...mta1m2...mf and n1n2... $n_t \alpha 2 n_2 \dots n_t$

We are interested in the case when m₁ and n₁ do not satisfy any of previous observed cases. If for some i one of them is satisfied then we observe polygons $M_1M_{j+1}...M_{j-1}$ and $N_1N_{j+1}...N_{j-1}$ where $M_{t+k}=M_k$ and $N_{t+k}=N_k$. Since $\frac{1}{1,m_2}=\frac{1}{2,n_2}$ and m_2,n_2 do not satisfy cases 1,2,3,4,5 then $m_2=2$ and $n_2=9$. Next, we have $1m_2...m_t=1297$ and $2n_2...n_t=2971$ but 1207 2971 therefore $m_1m_2...$ mtanin2...nt.

LEMMA 8: Let G be the grid obtained by tessellations 3^2 .4.3.4. Then: if A and B are congruent polygons then $r(A)\alpha r(B)$.

PROOF: Equivalence classes with respect to relation p are:

||| ={2,4,5,9,11,13,16,18} (see fig.4 (see fig.4)



initial vertex

f(P) = 13 12 7 15 10 9 13 9 13 16 8 14 19 0 18 11 10 16 11 18 6 0 18 11 5



VECTORS: 10 11...18 19 are opposite : 0 1... 8 9 for

t: 3 4 5 6 7 8 9 10 k(t): 1 2 1 3 6 17 35 101

Fig. 4.

Let $M=M_1M_2...M_t$ and $N=N_1N_2...N_t$ are congruent polygons and $r(M)=m_1m_2...m_t$ and $r(N)=n_1n_2...n_t$. Case of interest is:

case: $m_1 \in [l]$, $n_1 \in [l]$ then $m_1 m_2 \dots m_t \alpha 1 m_2 \dots m_t$ and $n_1 n_2 \dots n_t \alpha 1 1 n_2 \dots n_t$. Let us observe polygons $M' = ABM_3 \dots M_t$ and $N' = BAN_3 \dots N_t$ (see fig.4a): ($r(M') = 1m_2 \dots m_t$ and $r(N') = 11n_2 \dots n_t$)). Since M' and N' are congruent, there exist isometry S which maps plane into itself such that $S(M') = S(ABM_3 \dots M_t) = S(A)S(B)S_1(3)$. S(Mt)=BANg...Nt=N'. But then S is either

1) reflection in line s which is symmetry axes of segment [AB]

or 2) half turn with centre in middle of segment [AB]. If S is reflection then images of edges denoted by

broken line (---) do not belong to grid G; therefore ed-iges of polygon BAN3...N' can be some of edges denoted with ····. Since polygonis conected, we conclude n≤7.But for n≤7 there are thirteen different α-equivalence classes and representatives of this classes are not congruent polygons so statement follows. In the case when S is half turn, proof is analogous.

For grid G obtained by tessellation 3⁴.6 (see fig.5) words which correspond to congruence polygons do not have to be α -equivalent. For example: for congruent triangles A and B (as it is shown in fig. 5) $r(A)\alpha r(B)$ but there is no isometry which 1) maps grid G into itself, 2) maps A into B.



The proofs of following lemmas are omited, since they are analogous to proofs of Theorem 1 and Theorem 2. LEMMA 9: Let G be the grid obtained by tesselations 3.4.6.4, Then: if M and N are congruent polygons in grid G then $r(M)\alpha r(N)$.

PROOF: Equivalence classes with respect to relation p are:



LEMMA 10: Let G be the grid obtained by tesselation 3.6.3.6. Then: if M and N are polygons in grid G then $r(M)\alpha r(N)$.

PROOF: There exist only one equivalence classes with respect to relation p.



f(P) = 10 1 2 11 2 0 1 2 3 4 7 6 11 10 6 3 4 7 9 8 7 9 0 4 5 0 9 8

LEMMA 11: Let G be the grid obtained by tessellations 3.12^2 . Then if M and N are congruent polygons in grid G then $r(M)\alpha r(N)$.

PROOF: Equivalence classes with respect to relation ρ are:



LEMMA 12: Let G be the grid obtained by tessellation 4.6.12 then: if M and N are congruent polygons in grid G then $r(M)\alpha r(N)$.

PROOF: Equivalence classes with respect to rellation $\ensuremath{\rho}$ are:



 t:
 4 5 6 7 8 9 10 11 12 13 14 15 16

 k(t):
 1 0 1 0 1 0 1 0 3 0 2 0 9

Fig. 9.

LEMMA 13. Let G be the grid obtained by tessellation 4.8^2 then: if M, N are congruent polygons in grid G then $r(M)\alpha r(N)$.

PROOF: Equivalence classes with respect relation ρ are:



Let grid G be obtained by one of semi-regular tessellations $3^3.4^2$, $3^2.4.3.4$, 3.4.6.4, 3.6.3.6, 3.12^2 , 4.6.12, 4.8^2 then from lemmas 6-13 follows:

rig. iv.

THEOREM 1: Circuits ${\rm C}_1$ and ${\rm C}_2$ in grid G are isomorphic circuits iff they are congruent circuits.

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